

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 10017349-1

IN RE APPLICATION OF: U.S. Patent No. 6,910,142

USPTO CONFIRMATION CODE: 1128

APPLICATION NO.: 09/917,413

FILED: Jul. 28, 2001

EXAMINER: Lynne H. Browne

GROUP ART UNIT: 2116

FOR: SYSTEM FOR DETECTION AND ROUTING OF PLATFORM EVENTS IN A
MULTI-CELL COMPUTER

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number
09/917,413, now Patent Number 6,910,142.

A certificate of correction under 35 USC 254 is respectfully requested in the
above-identified patent.

☒ All errors were the fault of the USPTO, no fee required. In the event that a further
fee is required, please charge the amount to Deposit Account No. 082025.

☐ All errors were the fault of the applicant and, accordingly, please charge **\$100.00**
to our Deposit Account No. 082025. In the event that a further fee is required, please
charge the amount to the same Deposit Account.

☐ The errors were the fault of both the applicant and USPTO and, accordingly,
please charge **\$100.00** to our Deposit Account No. 082025. In the event that a further fee
is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application
are as follows:

In column 8, lines 57-67, column 9, lines 1-13, and column 10, lines 1-11, delete "18. In a computer system including at least one cell having an operating system associated therewith, and including an access panel that provides access to peripheral I/O cards controlled by the operating system, a method for minimizing boot time for the operating system by avoiding scanning for non-existent devices, the method comprising the steps of: notifying a supervisory processor that an intrusion event has occurred when said access panel has been opened; sending notification of said intrusion event from the supervisory processor to a local service processor; writing a message from the local service processor indicating the intrusion event; issuing a call from the operating system to retrieve the message indicating the intrusion event; sending a notification to the operating system indicating the slot associated with the intrusion event; storing a pending interrupt in a register in response to notification, from the local service processor, that the access panel is open; and upon booting said operating system, checking for said pending interrupt to determine whether said intrusion event occurred since the last boot; whereby, if no said intrusion event is indicated upon booting said operating system, boot time for the operating system is minimized by avoiding scanning for non-existent devices.

19. The method of claim 18, wherein the message from the local service processor indicating the intrusion event is written to RAM.

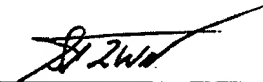
20. The method of claim 19, wherein a call is issued from the operating system to a firmware routine to get the message indicating the intrusion event."

(Notice of allowance dated Feb. 18, 2005, page 4, line 2)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

6/17/09
DATE


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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 6,910,142
APPLICATION NO. : 09/917,413
ISSUE DATE : Jun. 21, 2005
INVENTOR(S) : Edward A Cross, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 8, lines 57-67, column 9, lines 1-13, and column 10, lines 1-11, delete "18. In a computer system including at least one cell having an operating system associated therewith, and including an access panel that provides access to peripheral I/O cards controlled by the operating system, a method for minimizing boot time for the operating system by avoiding scanning for non-existent devices, the method comprising the steps of: notifying a supervisory processor that an intrusion event has occurred when said access panel has been opened; sending notification of said intrusion event from the supervisory processor to a local service processor; writing a message from the local service processor indicating the intrusion event; issuing a call from the operating system to retrieve the message indicating the intrusion event; sending a notification to the operating system indicating the slot associated with the intrusion event; storing a pending interrupt in a register in response to notification, from the local service processor, that the access panel is open; and upon booting said operating system, checking for said pending interrupt to determine whether said intrusion event occurred since the last boot; whereby, if no said intrusion event is indicated upon booting said operating system, boot time for the operating system is minimized by avoiding scanning for non-existent devices.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

This collection of information is required by 37 CFR 1.322, and 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 2 of 2

PATENT NO. : 6,910,142
APPLICATION NO. : 09/917,413
ISSUE DATE : Jun. 21, 2005
INVENTOR(S) : Edward A Cross, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

19. The method of claim 18, wherein the message from the local service processor indicating the intrusion event is written to RAM.
20. The method of claim 19, wherein a call is issued from the operating system to a firmware routine to get the message indicating the intrusion event.”.

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